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X		us	2002/	0100029	07/25/02	Bowen	717	140			
DC1		us	2002/	0133725	09/19/02	Roy et al.	713	202			
X		US:	2002/	0174411	11/21/02	Feng et al.	716	12			
R		US	2002/	0188885	12/12/02	Sihlbom et al.	713	600			
- W		US	2003/	0025132	02/06/03	Tobey	257	202			
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Q	Borgatti, Michele et al., "A Reconfigurable System Featuring Dynamically Extensible Embedded Microprocessor, FPGA, and Customizable I/O," IEEE Journal of Solid-State Circuits, Vol. 38, No. 3, March 2003, pp. 521-529: Hauser, John R. et al., "Garp: A MIPS Processor with a Reconfigurable Coprocessor," IEEE 1997, pp.										
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 (Modified)										ATTY. DOCKET NO.		SERIAL N	SERIAL NO.	
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT									TIONS FOR	RPS920030110US1		To Be Assigned		
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Q.		5	9	3	7	1	7	9	08/10/99	Swoboda	395	500		
DC.		6	0	4	1	1	4	0	03/21/00	Binns et al.	382	209		
EL.		6	4	6	7	0	0	9	10/15/02	Winegarden et al.	710	305		
EX.		6	5	0	4	3	9	8	01/07/03	Lien et al.	326	41		
DE		6	5	0	4	3	9	9	01/07/03	Pani et al.	326	41		
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